

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-373	SERIAL NO. 09/388,357		
<div style="position: relative; width: 100px; height: 100px; margin: 0 auto;"> <div style="position: absolute; top: 0; left: 0; right: 0; bottom: 0; border: 1px solid black; border-radius: 50%; text-align: center;"> <div style="position: absolute; top: 5px; left: 5px; font-size: 10px;">O I P E</div> <div style="position: absolute; top: 10px; left: 10px; font-size: 10px;">OCT 31 2004</div> <div style="position: absolute; top: 15px; left: 15px; font-size: 10px;">PATENT & TRADEMARK OFFICE</div> </div> </div>				LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)			
				APPLICANT Luan C. Tran			
				FILING DATE 09/01/99	GROUP 2813		
U.S. PATENT DOCUMENTS							
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA						
	AB						
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	AJ						
	AK						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
		AL					
		AM					
		AN					
		AO					
		AP					
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
AR		S. Wolf, <i>Silicon Processing for the VLSI Era</i> , Volume 3 - The Submicron MOSFET. ©1995 Lattice Press, pages 330, 331, 366 and 367.					
AS							
AT							
EXAMINER		DATE CONSIDERED					
Luan C. Tran		7/29/01					
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